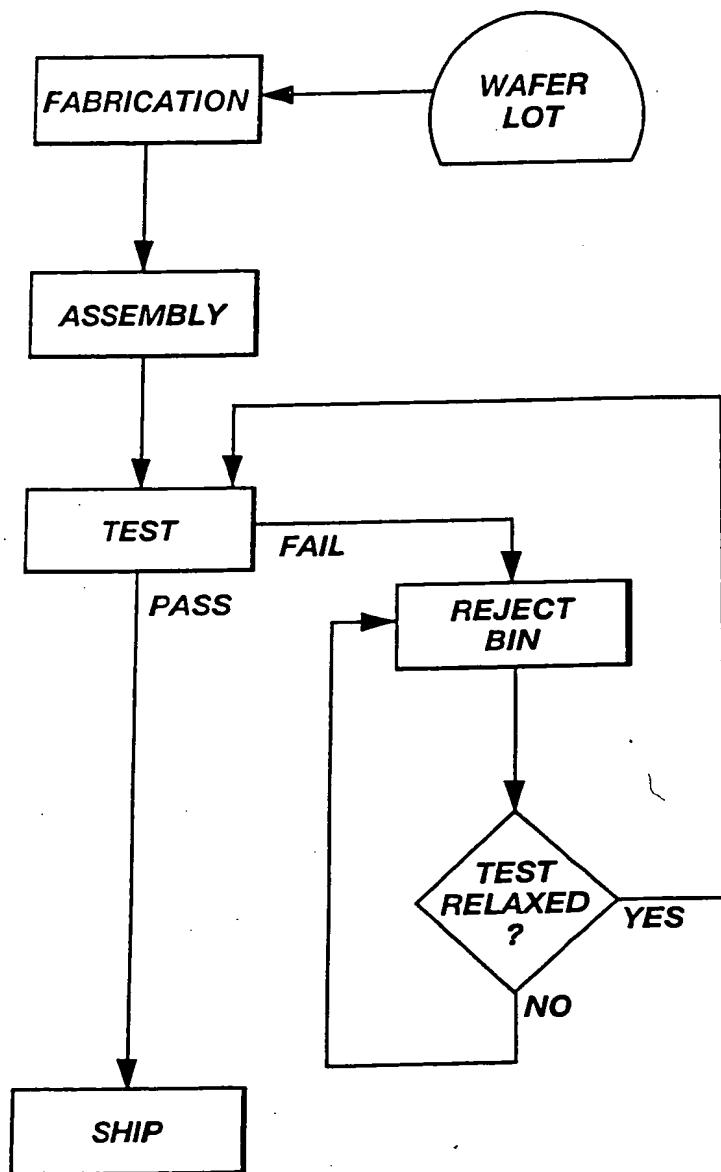


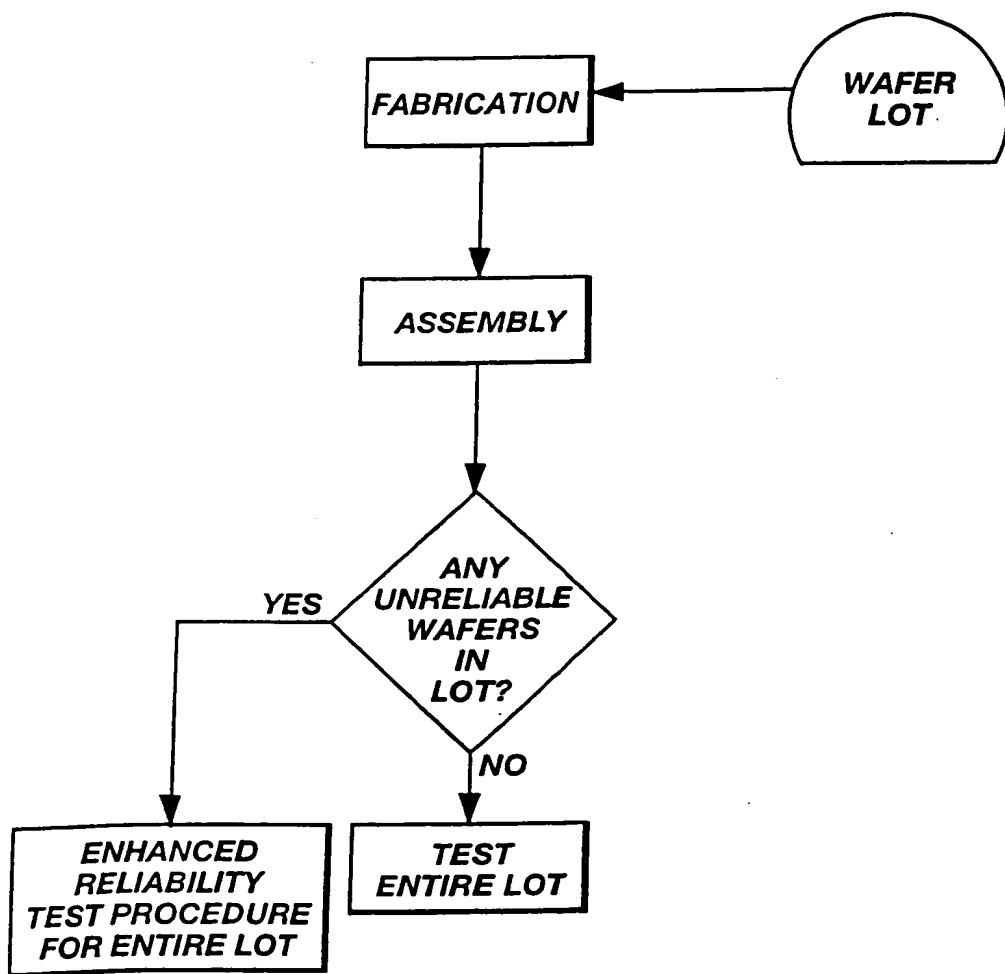
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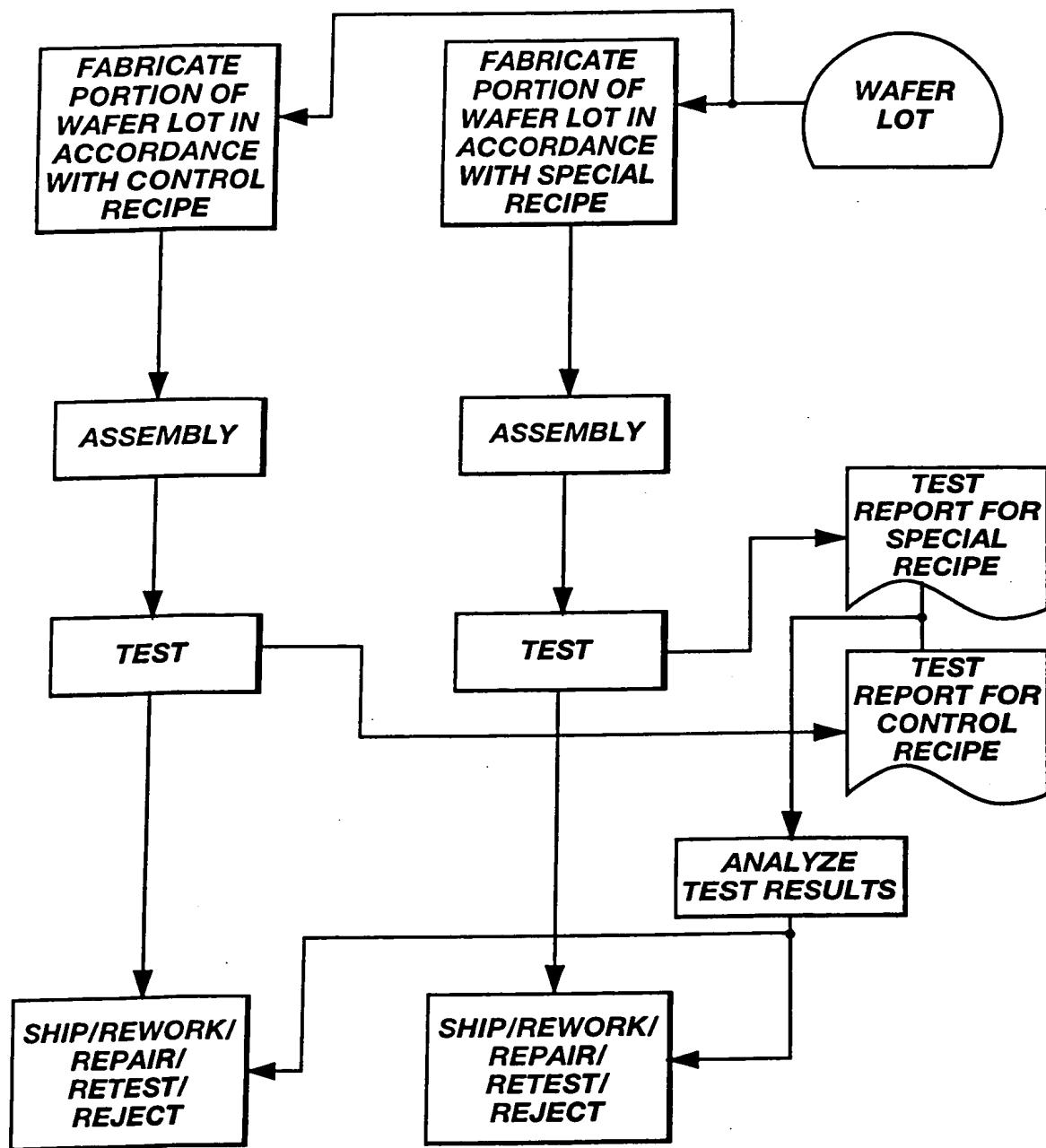
*Fig. 1*  
(PRIOR ART)

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**Fig. 2**  
**(PRIOR ART)**

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**FIG. 3**  
(PRIOR ART)

0004/8

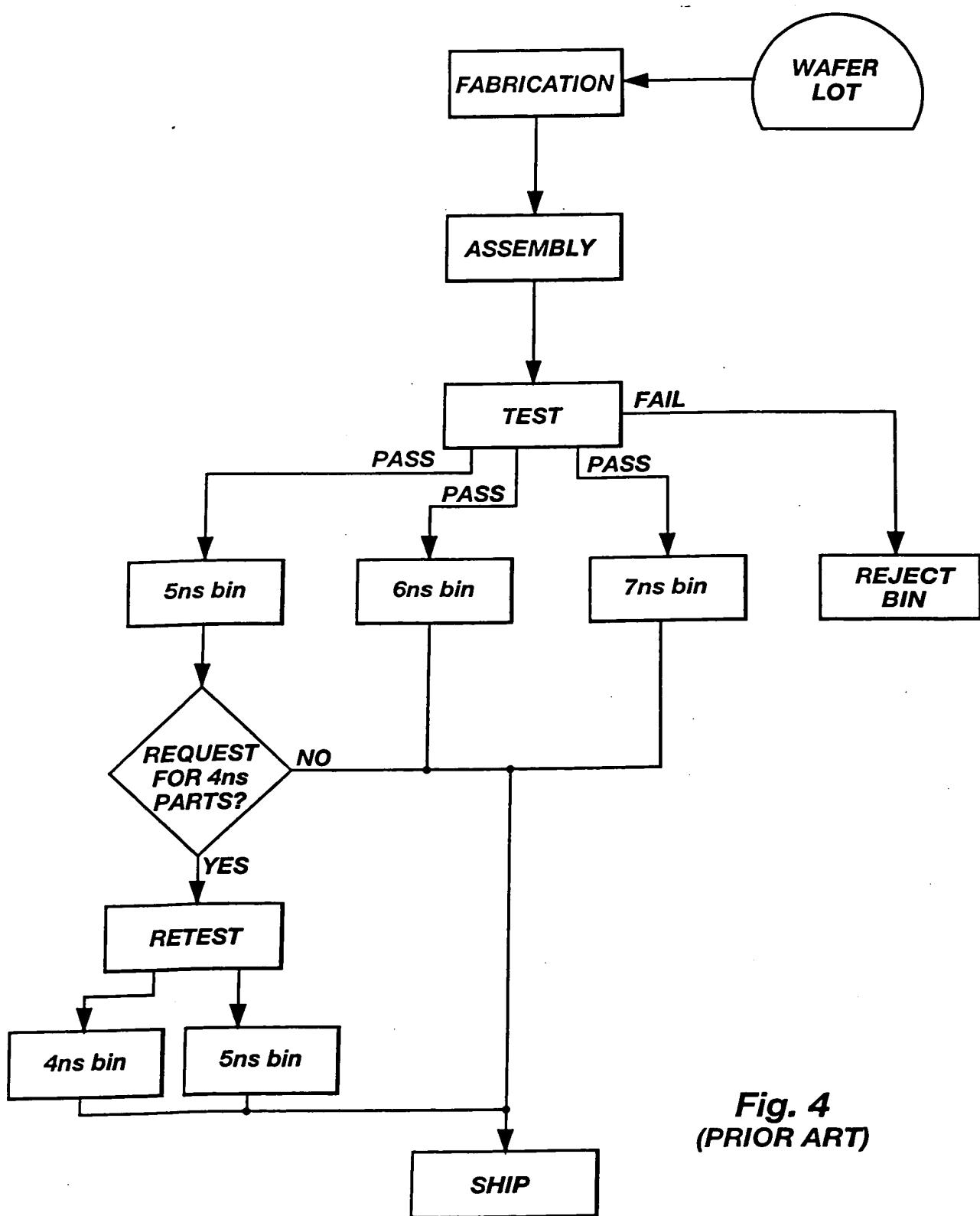


Fig. 4  
(PRIOR ART)

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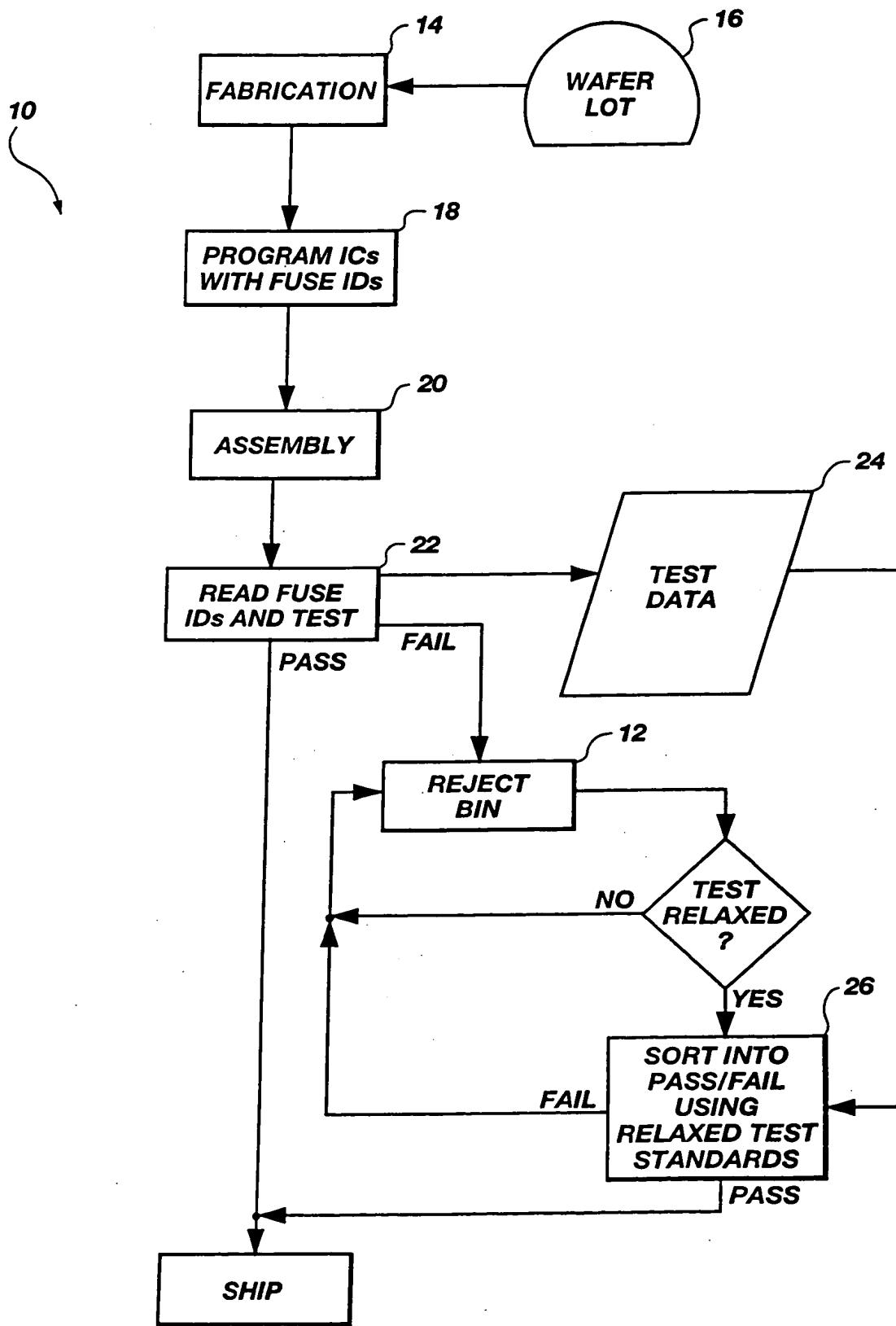


Fig. 5

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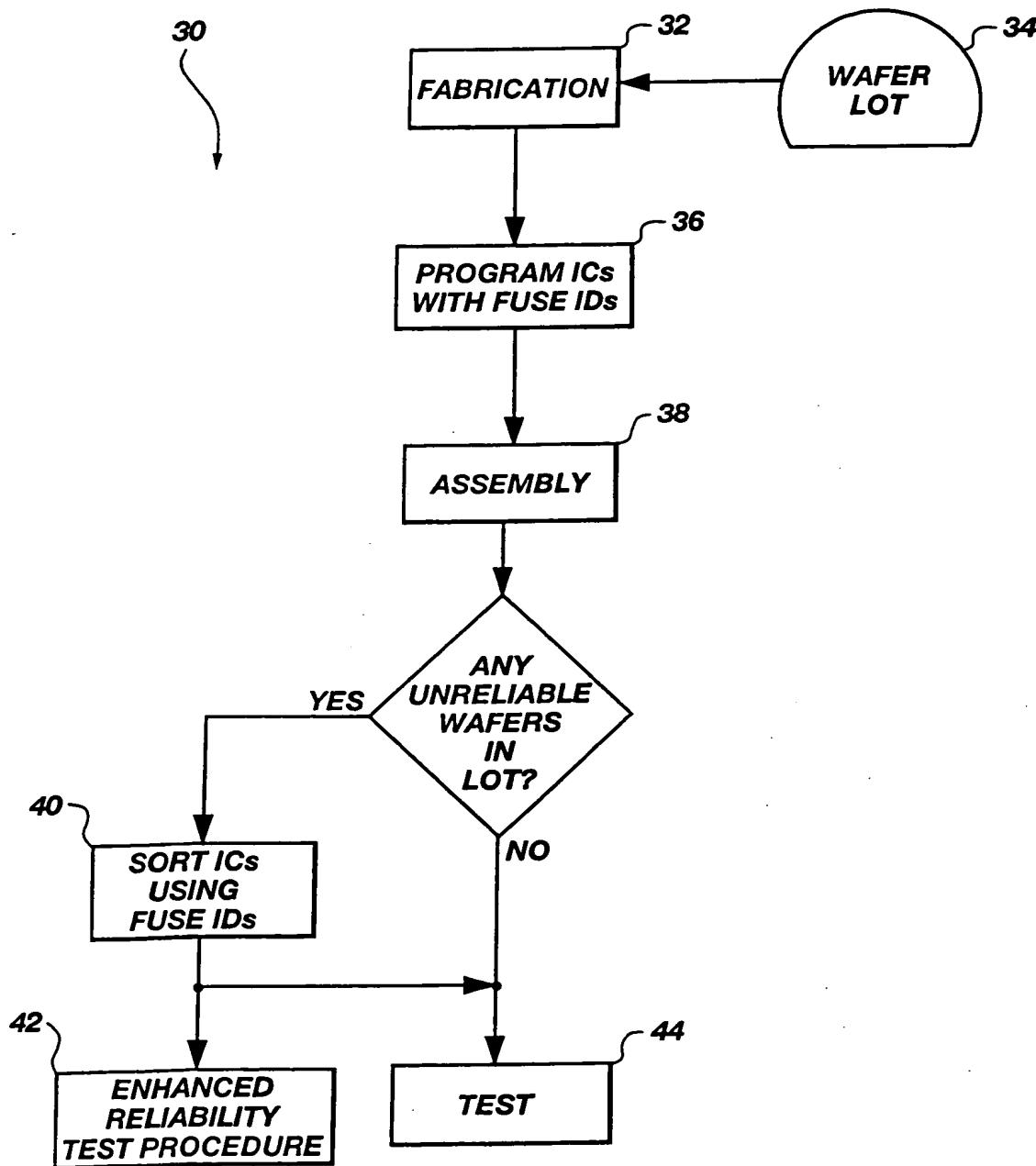


Fig. 6

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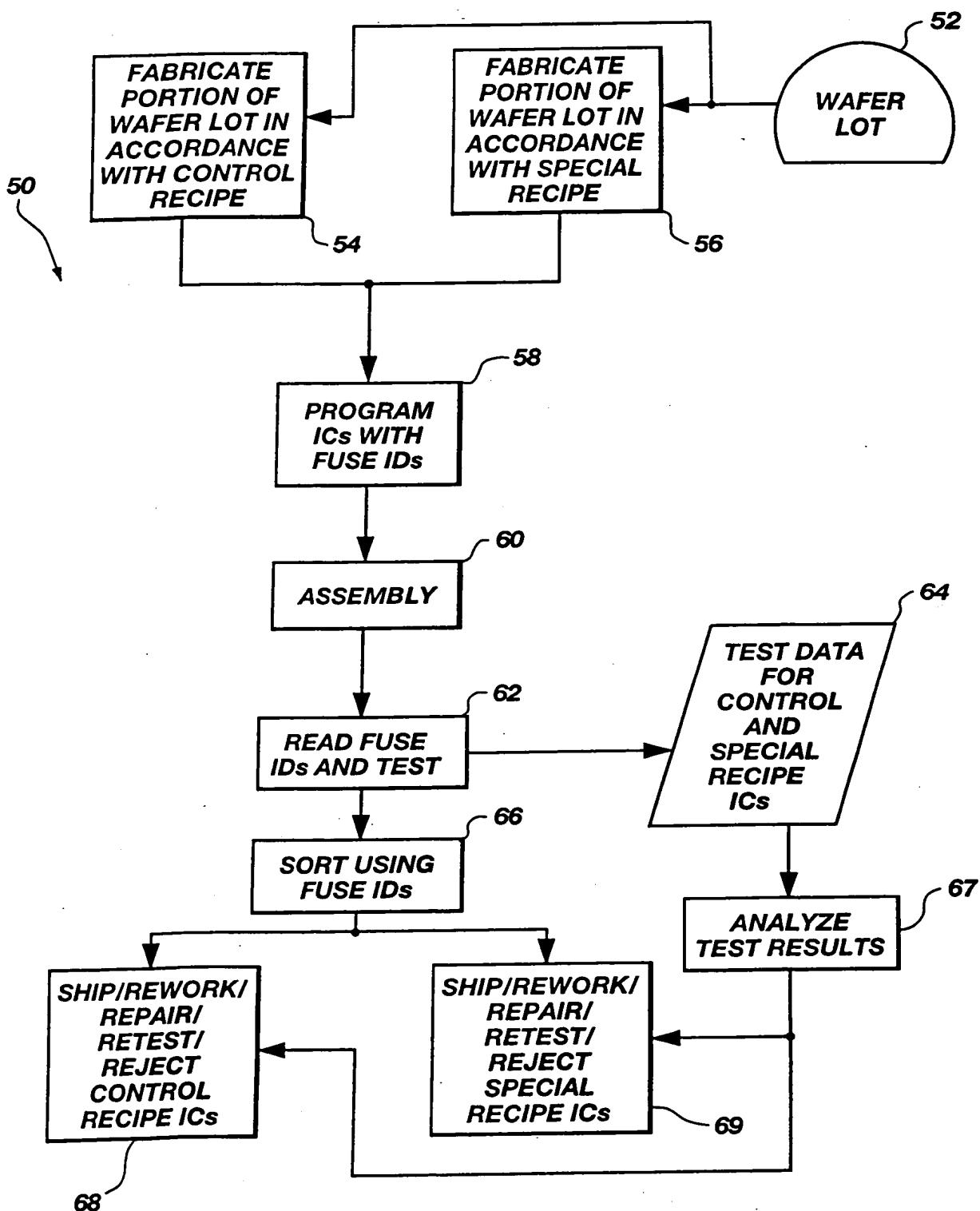


Fig. 7

TITLE: METHOD FOR SORTING INTEGRATED  
CIRCUIT DEVICES  
Inventor: Raymond J. Beffa  
Docket No.: 3037.10US

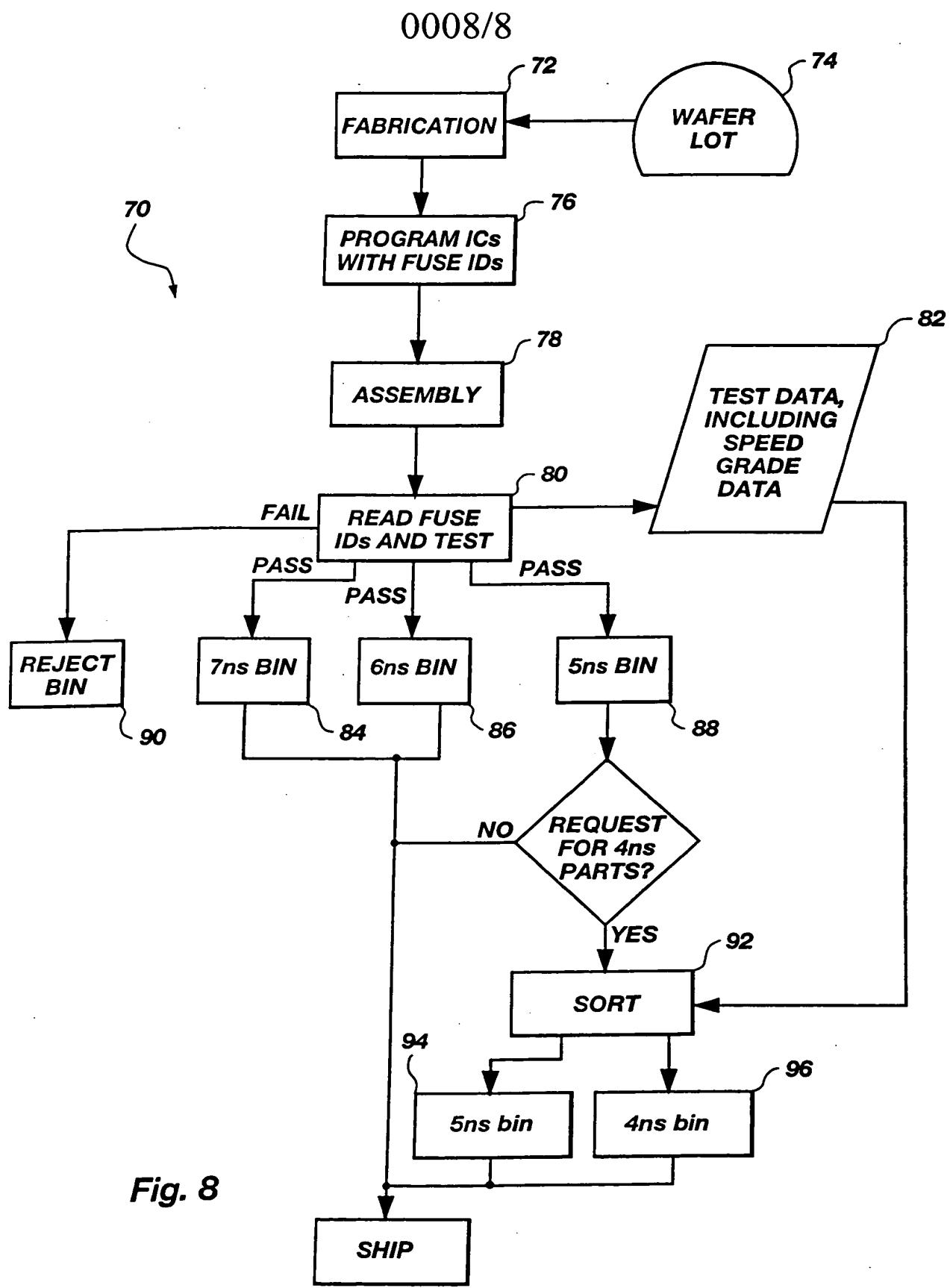


Fig. 8